Amendments to the Specification:

Please amend paragraphs [0169] – [0170] as follows:

[0169] ~~termined frequency and corresponding to the sub-block marks 228 and 229. On detecting these marks 228 and 229, the detector 377 generates a timing signal. The timing signal is output from the sub-block mark detector 377 to an address decoder 378.~~

[0170] ~~As described above, a steep edge of a saw-tooth like wobble has its polarity inverted depending on whether it represents "1" or "0" of address information. In accordance with the output of the high-pass filter 375, an address information detector 376 detects this polarity inversion and sends out a bit stream to the address decoder 378. On receiving this bit stream, the address dec~~Next, a twelfth preferred embodiment of the present invention will be described with reference to FIG. 18.

Please amend paragraph [0180] and create new paragraph [0180A] as follows:

[0180] A sub-block mark detector 377 detects the wobble components having a predetermined frequency and corresponding to the sub-block marks 228 and 229. On detecting these marks 228 and 229, the detector 377 generates a timing signal. The timing signal is output from the sub-block mark detector 377 to an address decoder 378.

[0180A] As described above, a steep edge of a saw-tooth like wobble has its polarity inverted depending on whether it represents "1" or "0" of address information. In accordance with the output of the high-pass filter 375, an address information detector 376 detects this polarity inversion and sends out a bit stream to the address decoder 378. On receiving this bit stream, the address decoder 378 decodes the address information in response to the timing signal that has been output from the sub-block mark detector 377.